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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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IN RE APPLICATION:



Yutaka TERADA, et al.

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SERIAL NO.:

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Group 2700

FILING DATE:

27 April 1999

RE: INPUT CIRCUIT AND OUTPUT CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §§ 1.56 and 1.98, Applicant(s) wish(es) to bring to the attention of the U.S. Patent and Trademark Office the reference(s) listed on the enclosed Form PTO-1449. The references were cited in the corresponding European Search Report. Copies of the listed reference(s) and search report are attached.

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 13-0203. A duplicate copy of this communication is enclosed for accounting purposes.

Respectfully submitted,  
McDERMOTT, WILL & EMERY

Date: 01 October 1999

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